



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2813
Serial No.: 09/451,665 Examiner : Laura Schillinger
Filed : November 30, 1999
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD
THEREOF

#11
3/8/02
Darius
MAIL ROOM

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Sir

In response to the Official Action dated October 30, 2001,
Paper No. 10, in the above-referenced application, kindly
consider the following remarks.

REMARKS

Reconsideration and allowance of the above-referenced
application are respectfully requested.

Claims 1, 2, 4, 5, 7-11, 22, 23, 25, 26, 28-32, 43-47, 52-
55, and 61-82 stand rejected under 35 U.S.C. 102(e) as allegedly
being anticipated by Zhang '000. This contention is
respectfully traversed, and it is respectfully suggested that
all of the claims should be in condition for allowance. The

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
deposited with the United States Postal Service as first class mail with
sufficient postage on the date indicated below and is addressed to the
Commissioner for Patents, Washington, D.C. 20231.

January 30, 2002
Date of Deposit

Signature *Susan Regan*

Susan Regan
Typed or Printed Name of Person Signing Certificate